

The Design of Falcon

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PQShield

Cryptography Standard Design Seminar (02/07/2023)

This talk:

- ✓ High-level description
- ✓ Side-channel security
- ✓ Deployment
- ✓ Algorithmics (a bit)
- ✗ Implementation
- ✗ Cryptanalysis

Hash-then-Sign

Keygen(1^λ)

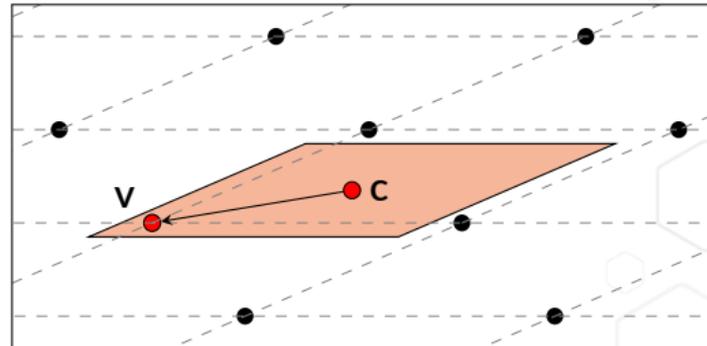
- 1 Gen. matrices **A**, **B** such that:
 - > **A** is pseudorandom
 - > $\mathbf{A} \cdot \mathbf{B} = \mathbf{0}$
 - > **B** has small coefficients
- 2 $\text{pk} := \mathbf{A}, \text{sk} := \mathbf{B}$

Sign(msg, sk = **B**)

- 1 Compute **c** such that $\mathbf{A} \cdot \mathbf{c} = H(\text{msg})$
- 2 $\mathbf{v} := \mathbf{B} \lfloor \mathbf{B}^{-1} \mathbf{c} \rfloor$
- 3 $\text{sig} := \mathbf{s} = (\mathbf{c} - \mathbf{v})$

Verify(msg, pk = **A**, sig = **s**)

Check (**s** short) & $(\mathbf{A} \cdot \mathbf{s} = H(\text{msg}))$



Keygen(1^λ)

- 1 Gen. matrices \mathbf{A} , \mathbf{B} such that:
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Sign(msg, sk = \mathbf{B})

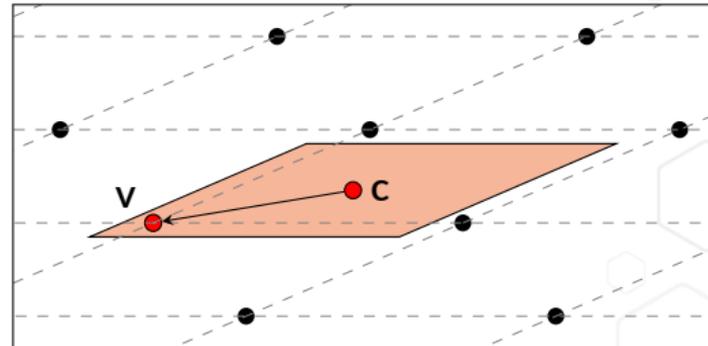
- 1 Compute \mathbf{c} such that $\mathbf{A} \cdot \mathbf{c} = H(\text{msg})$
- 2 $\mathbf{v} := \mathbf{B} \lfloor \mathbf{B}^{-1} \mathbf{c} \rfloor$
- 3 $\text{sig} := \mathbf{s} = (\mathbf{c} - \mathbf{v})$

→ **Correctness:** easy

→ **Security:** Finding a short preimage \mathbf{s} of $H(\text{msg})$ should be difficult... or is it?

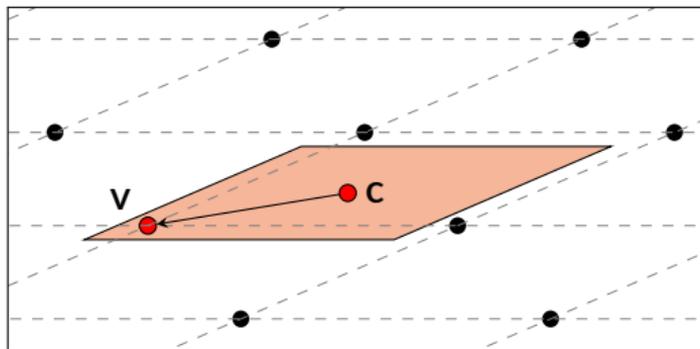
Verify(msg, pk = \mathbf{A} , sig = \mathbf{s})

Check (\mathbf{s} short) & ($\mathbf{A} \cdot \mathbf{s} = H(\text{msg})$)



Problem: The distribution of the signature \mathbf{s} is correlated to \mathbf{B}

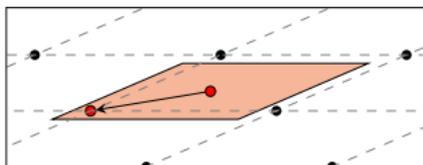
$$\mathbf{s} = \mathbf{c} - \mathbf{B} \left\lfloor \mathbf{B}^{-1} \mathbf{c} \right\rfloor \in \left[-\frac{1}{2}, \frac{1}{2} \right] \cdot \mathbf{B} \quad (1)$$



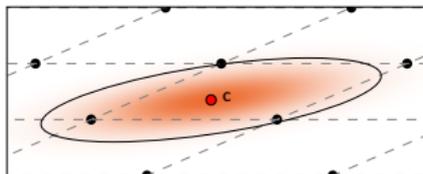
Given many signatures, \mathbf{B} can be recovered using techniques from Independent Component Analysis (ICA)

- 2006: key-recovery on NTRUSign and GGHSign
- 2012: key-recovery against NTRUSign countermeasures

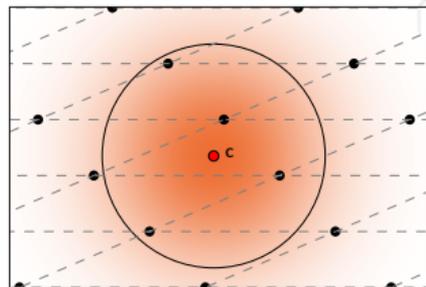
$$\mathbf{B} \lfloor \mathbf{B}^{-1} \mathbf{c} \rfloor$$



$$\mathbf{B} \lfloor \mathbf{B}^{-1} \mathbf{c} \rfloor_{\sigma_1}$$



$$\mathbf{B} \lfloor \mathbf{B}^{-1} (\mathbf{c} + \mathbf{M} \lfloor \mathbf{0} \rfloor_{\sigma_2}) \rfloor_{\sigma_1}$$



Indistinguishability: For appropriately chosen parameters, the rightmost procedure outputs a distribution close to a perfect Gaussian $D_{\Lambda(\mathbf{B}), \mathbf{c}, \sigma}$.

Consequence: these two worlds are indistinguishable (in the ROM)

- 1 Sample a short vector \mathbf{s} , then set $H(\text{msg}) = \mathbf{A} \cdot \mathbf{s}$
- 2 Compute $H(\text{msg})$, then use \mathbf{B} to sample a short preimage \mathbf{s} of $H(\text{msg})$

The GPV framework requires two ingredients:

- 1 A family of trapdoors (**A**, **B**)
- 2 A trapdoor sampler for computing a short preimage **s**

Falcon: our goal is to minimize the communication cost

NTRU trapdoors

Let $f, g, F, G \in \mathcal{R}$ such that:

$$fG - gF = q \quad (2)$$

$$h := g/f \bmod q \quad (3)$$

We set $\mathbf{A} = [1 \quad h]$ and $\mathbf{B} = \begin{bmatrix} g & G \\ -f & -F \end{bmatrix}$.

NTRU trapdoors

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 **Pseudorandomness of \mathbf{A} :** NTRU assumption.

 **Orthogonality:** One can easily show that $\mathbf{A} \cdot \mathbf{B} = \mathbf{0} \bmod q$.

 **Shortness of \mathbf{B} :** Given (f, g) , one can compute suitable (F, G) such that

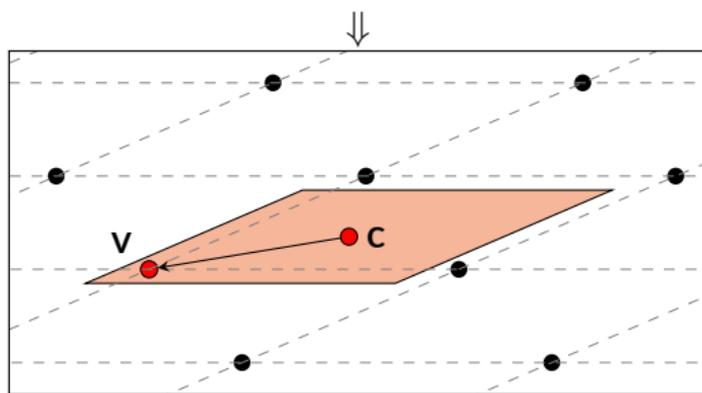
$$\left\| \text{Proj}_{\text{Span}(f, g)^\perp} (F, G) \right\| \approx \frac{q}{\|(f, g)\|} \quad (4)$$

It is optimal to take $\|(f, g)\| \approx 1.17\sqrt{q}$.

Computing a lattice point v close to the target c

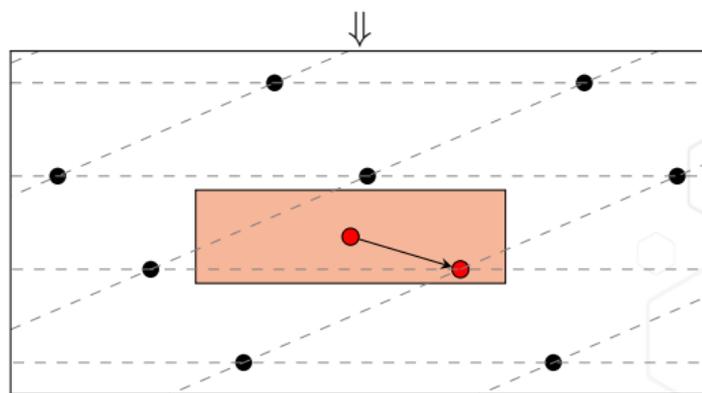
RoundOff(B, c)

- 1 $t \leftarrow c \cdot B^{-1}$
- 2 For $j \in \{n, \dots, 1\}$:
 - 1 $z_j \leftarrow \lceil t_j \rceil$
- 3 Return $v := z \cdot B$



NearestPlane(B, L, c)

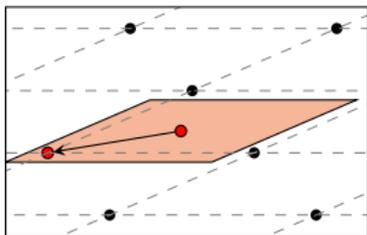
- 1 $t \leftarrow c \cdot B^{-1}$
- 2 For $j \in \{n, \dots, 1\}$:
 - 1 $z_j \leftarrow \lceil t_j + \sum_{i>j} (t_i - z_i)L_{i,j} \rceil$
- 3 Return $v := z \cdot B$



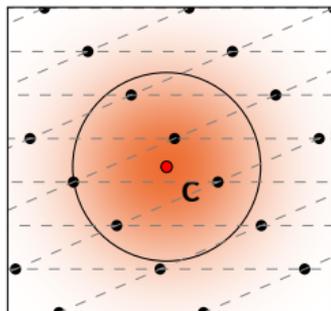
The second algorithm is better for security.

We combine NearestPlane with Gaussian rounding to obtain a discretized Gaussian.

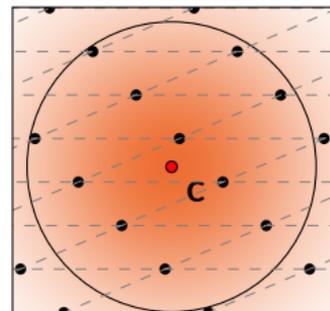
σ too small



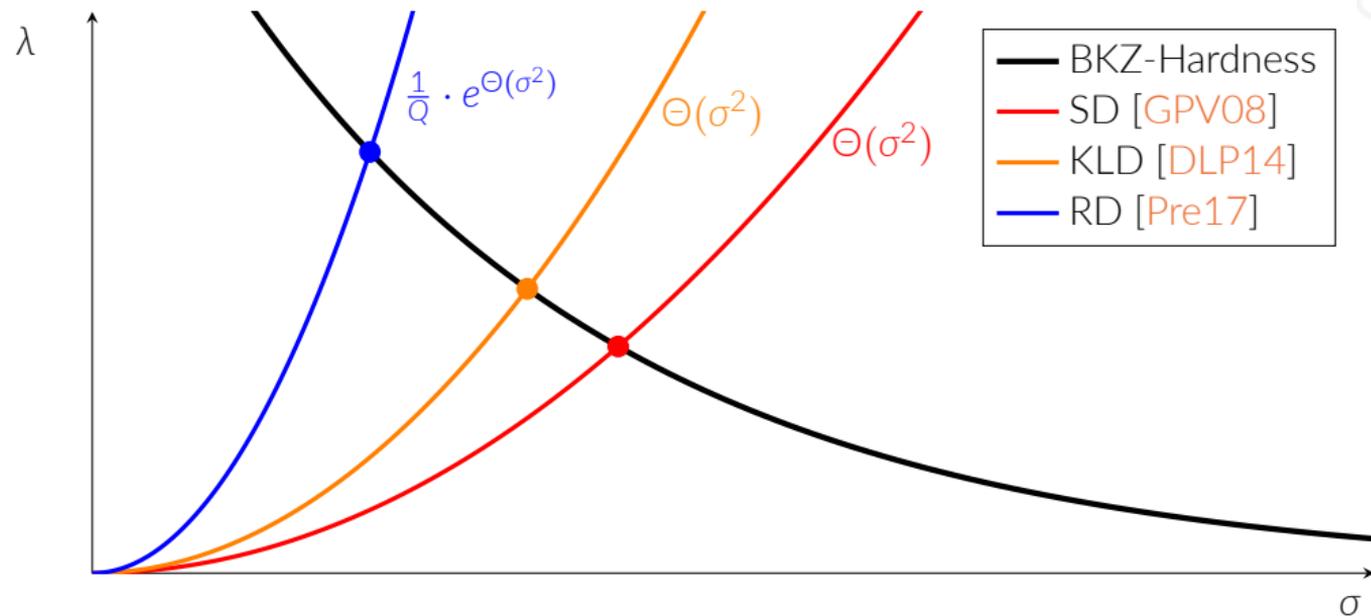
The "right" σ



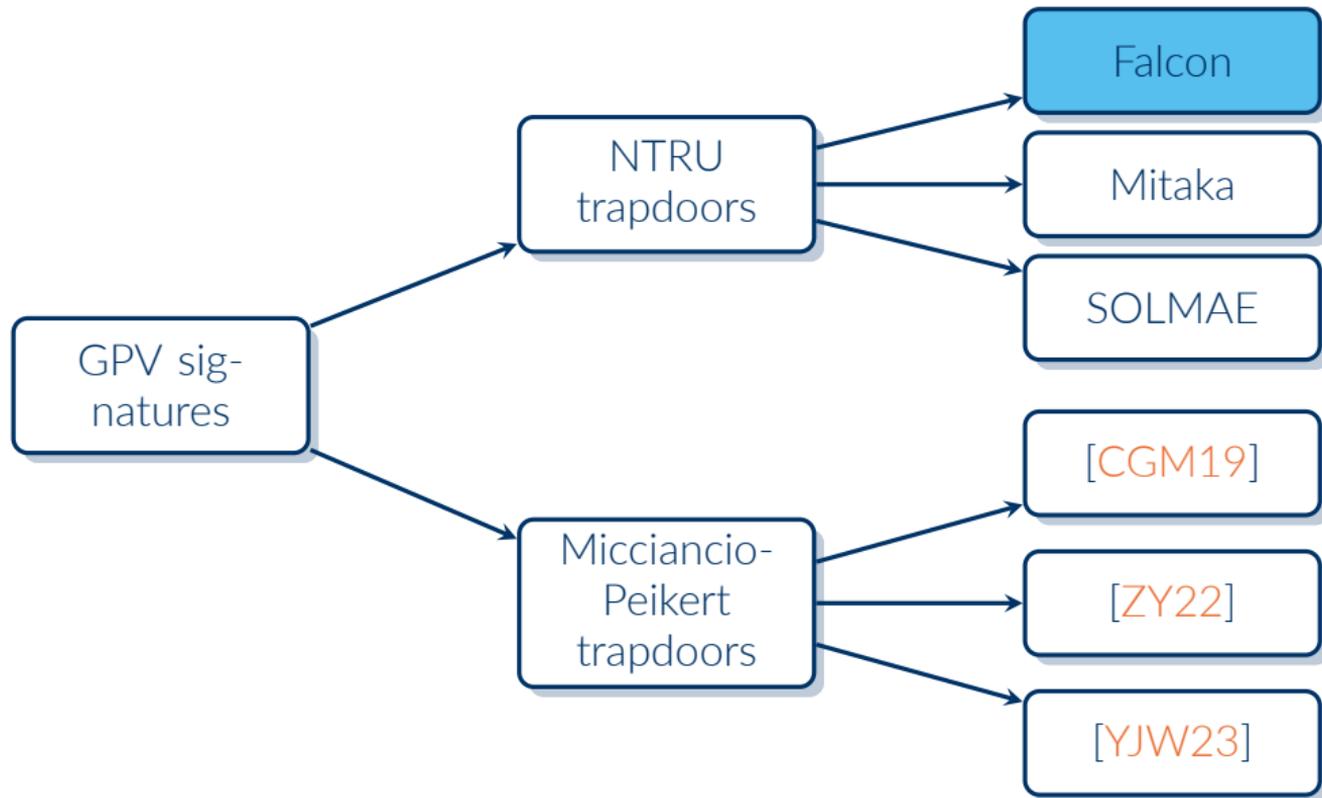
σ too large



- 1 σ too small \Rightarrow vulnerable to learning attacks [NR06, DN12]
- 2 σ too large \Rightarrow suboptimal for cryptography



For $Q = 2^{64}$, we gain about 30 bits of security (compared to the SD).



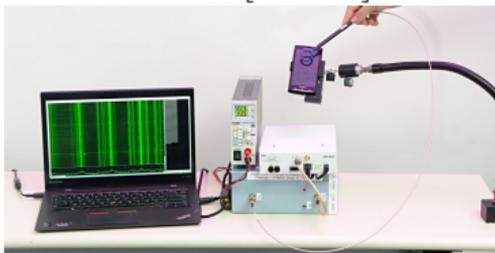
Falcon = GPV framework + NTRU trapdoors + Fast Fourier sampler + optimizations

Side-Channel Attacks

Power analysis attacks [KJJ99]



Electromagnetic attacks [Eck85]



Timing attacks [Koc96]



Acoustic attacks [AA04]



Visual attacks [NIC+23]



And more...

In Falcon, a signature \mathbf{s} is distributed as a Gaussian.
The power consumption leaks information about the dot product $\langle \mathbf{s}, \mathbf{b}_0 \rangle$, or \mathbf{s} itself.

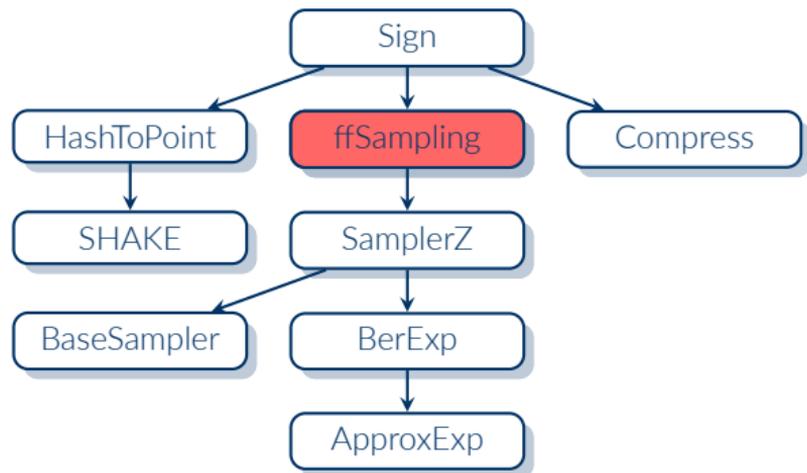
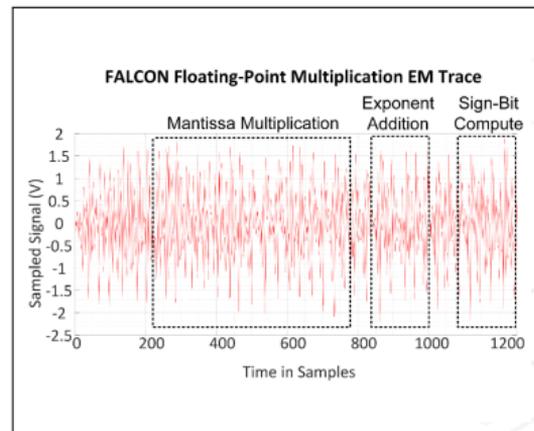
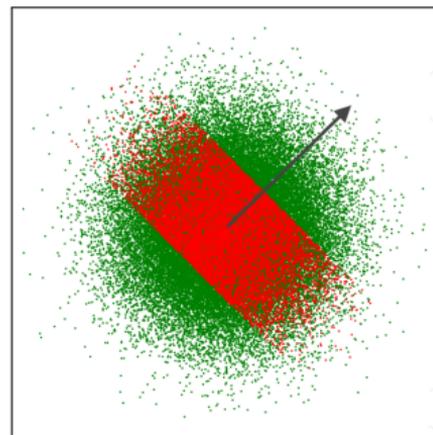
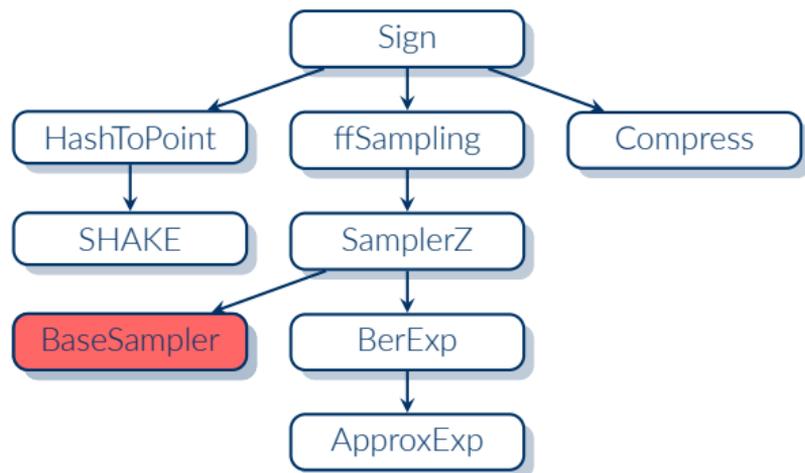


Figure 1: Flowchart of the signature



Learning \mathbf{s} directly

In Falcon, a signature \mathbf{s} is distributed as a Gaussian.
The power consumption leaks information about the dot product $\langle \mathbf{s}, \mathbf{b}_0 \rangle$, or \mathbf{s} itself.



Filtering $\langle \mathbf{s}, \mathbf{b}_0 \rangle$ close to zero

Figure 1: Flowchart of the signature

²The Hidden Parallelepiped Is Back Again: Power Analysis Attacks on Falcon [GMRR22]

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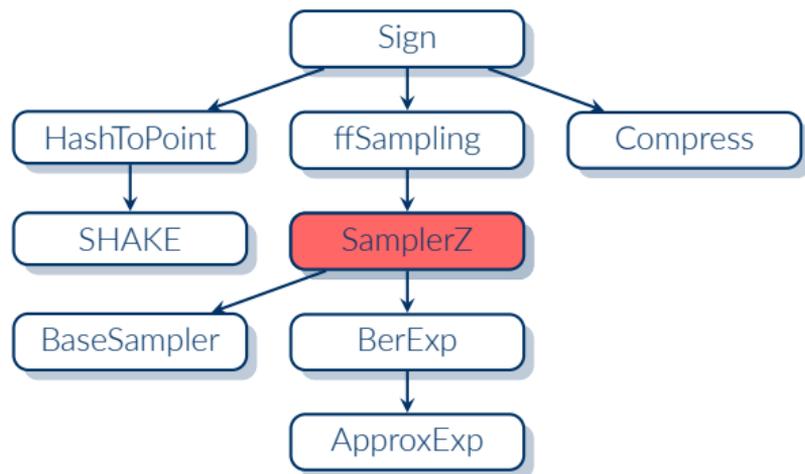
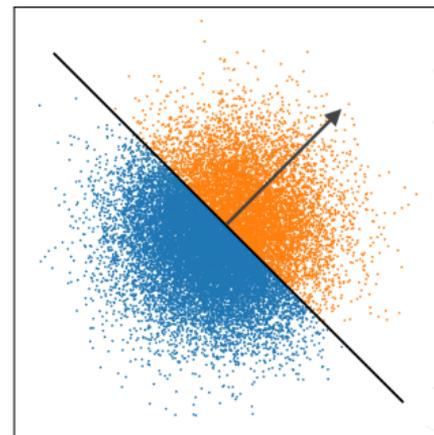


Figure 1: Flowchart of the signature



Filtering $\langle \mathbf{s}, \mathbf{b}_0 \rangle > 0$

Against timing attacks: make signing isochronous (“cryptographic constant time”)

- BaseSampler reads a full table
- BerExp implements rejection sampling via polynomial approximation

The signing procedure is isochronous assuming that some basic FPA operations are.

Protection beyond timing attacks?

- [GMRR22, ZLYW23] propose countermeasures but they are ad hoc and only make their attacks more expensive to mount
- In general, the most robust countermeasure is masking
 - Masking Falcon is going to be very difficult
 - If masking is important, use Raccoon (github.com/masksign/raccoon)

When to Deploy



Pros

- Compact public key and signature sizes
- Very fast verification
- Signing is also fast, but less than Dilithium

Cons

- Key generation and signing require FPA
 - Be mindful on devices with non-existent or variable-time FPA units
 - Say goodbye to masking
- Key generation and signing are complex to implement
- Key generation is slow-ish

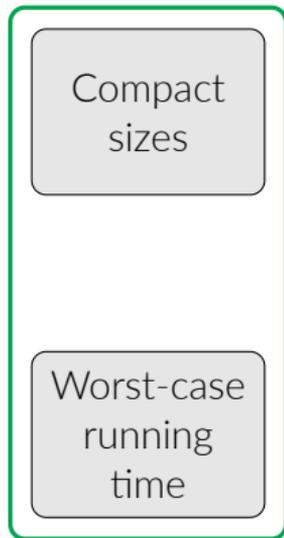
Compact
sizes

Verification
speed

Worst-case
running
time

Verification
memory





V2V

Verification speed

Verification memory

Drive (Quantum) Safe! – Towards Post-Quantum Security for V2V Communications [BMTR22]

“ Only signature schemes whose explicit certificate can be sent in five or less fragments can be used in the *True Hybrid* design. [...] Falcon is the only viable scheme. ”

TLS

Compact
sizes

Verification
speed

Worst-case
running
time

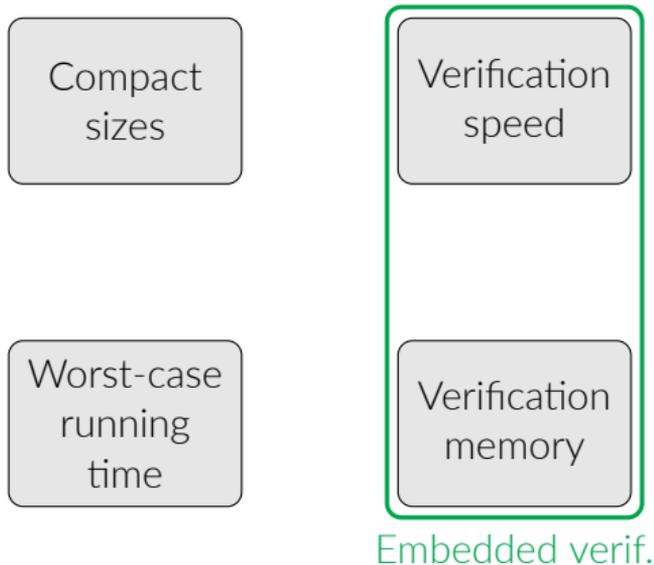
Verification
memory

Post-Quantum Authentication in TLS 1.3: A Performance Study [SKD20]

“ The PQ algorithms with the best performance for time-sensitive applications are Dilithium and Falcon. ”

NIST's pleasant post-quantum surprise [Wes22] recommends:

- Falcon for offline signature
- Dilithium for handshake



FPGA Energy Consumption of Post-Quantum Cryptography [BKG22]

“ For signature verification, Falcon provides the lowest energy consumption, highest throughput, and lowest transmission size [compared to Dilithium and SPHINCS+]. ”

Verifying Post-Quantum Signatures in 8 kB of RAM [GHK⁺21]

“ On Cortex-M3, [Falcon’s] overall memory footprint is about 6.5 kB. ”

DNSSEC

Compact
sizes

Verification
speed

Worst-case
running
time

Verification
memory

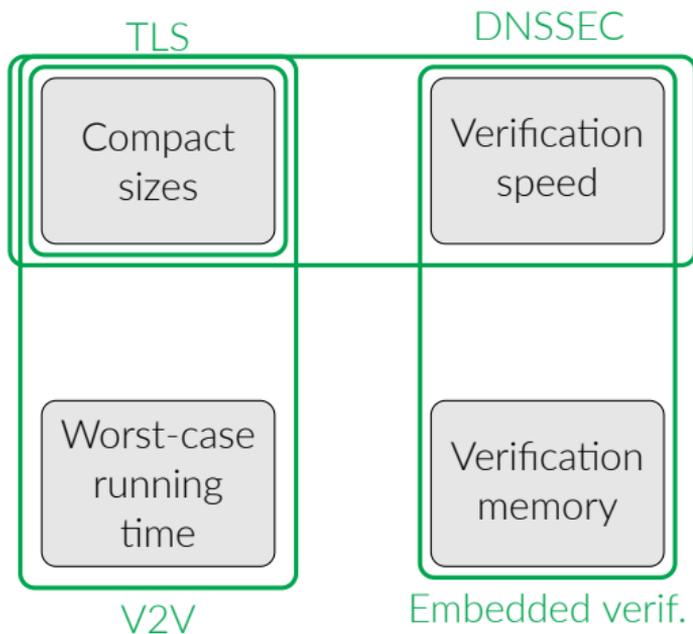
Retrofitting Post-Quantum Cryptography in Internet Protocols:

A Case Study of DNSSEC [MdJvH⁺20]

“ [...] the performance of Falcon-512 is closest to the current algorithms and meets the requirements of DNSSEC. ”

Post-Quantum Signatures in DNSSEC via Request-Based Fragmentation [GS22]

“ [...] Falcon-512 may be the most suitable option currently available to be standardized for DNSSEC. ”



Suitable applications:

- V2V
- TLS certificates
- Verification on embedded devices
- DNSSEC
- ...

Questions?

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